



Receipt
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PATENT
81225-245694

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
NAOKI KUSUNOKI, et al.

Serial No.: 09/812,365

Filed: March 20, 2001

For: **METHOD OF SIMULATION OF
PRODUCTION PROCESS OF SEMICONDUCTOR
DEVICE AND SIMULATOR THEREOF**

Art Unit: 2123

Examiner: Not yet assigned

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SEP 14 2001
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Office of Initial Patent Examination, Customer Service Center, Washington D.C. 20231, on June 7, 2001.

By: _____

Roger B. Wise, Reg. No. 31,204

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

Dear Sir:

We have received a Filing Receipt for the above-identified patent application. However, the Filing Receipt is incorrect because the name of one of the inventors was misspelled. Please correct the name of "Nobuoshi Aoki" to read as --**NOBUTOSHI AOKI**--.

Enclosed is a photocopy of the Corrected Filing Receipt which indicates the requested correction.

It is believed this is a Patent Office error, and accordingly, no fee is enclosed. However, if a fee should be required, please charge it to Deposit Account No. 03-3975 of the undersigned attorney's firm. A copy of this Request is enclosed.

Respectfully submitted,
PILLSBURY WINTHROP LLP

Dated: June 7, 2001

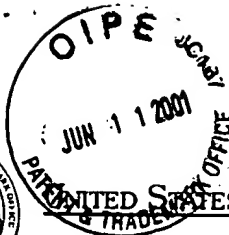
By: 

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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|---------------|-----------------|----------|------------|------------|
| 09/812,365 | 03/20/2001 | 2123 | 710 | PW 024 5694 | 23 | 20 | 3 |

CONFIRMATION NO. 1740

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Date Mailed: 05/29/2001

Mr. Roger R. Wise
PILLSBURY MADISON & SUTRO LLP
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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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~~Nobutoshi~~ Nobutoshi Aoki, Kanagawa-ken, JAPAN;
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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN P2000-85268 03/24/2000

If Required, Foreign Filing License Granted 05/26/2001

Projected Publication Date: 09/27/2001

Non-Publication Request: No

Early Publication Request: No

Title

Method of simulation of production process of semiconductor device and simulator thereof

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JUN 05 2001

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